

**AMENDMENTS TO THE DRAWINGS**

Please replace sheets 4, 5, and 6 of the drawings on record, which include Figs. 4b, 5a, and 5b, respectively, with the replacement sheets 4, 5, and 6 enclosed herewith.

In Fig. 4b, the input bit distribution has been changed from “[64:0]” to “[63:0]”.

In Fig. 5a, the input bit distributions have been changed from “[63:0]”, “[56:0]”, “[48:0]”, and “[40:0]” to “[31:0]”, “[23:0]”, “[15:0]”, and “[7:0]”, respectively.

In Fig. 5b the input bit distribution has been changed from “[64:0]” to “[31:0]” and the bit capacity of the line from 530 to 522a has been changed from “40” to “8”.



CRC Calc (8-5 bytes) ~ 304

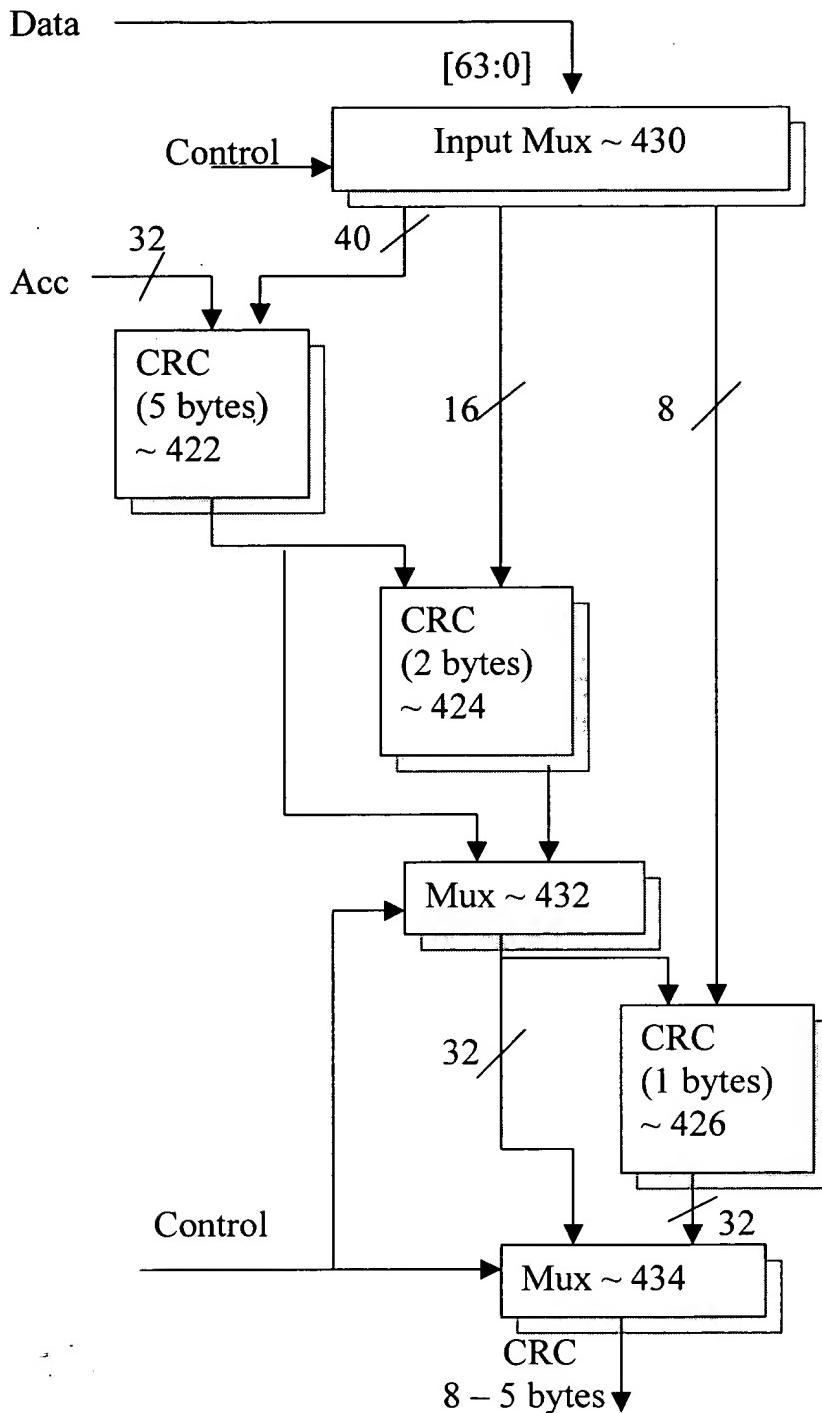
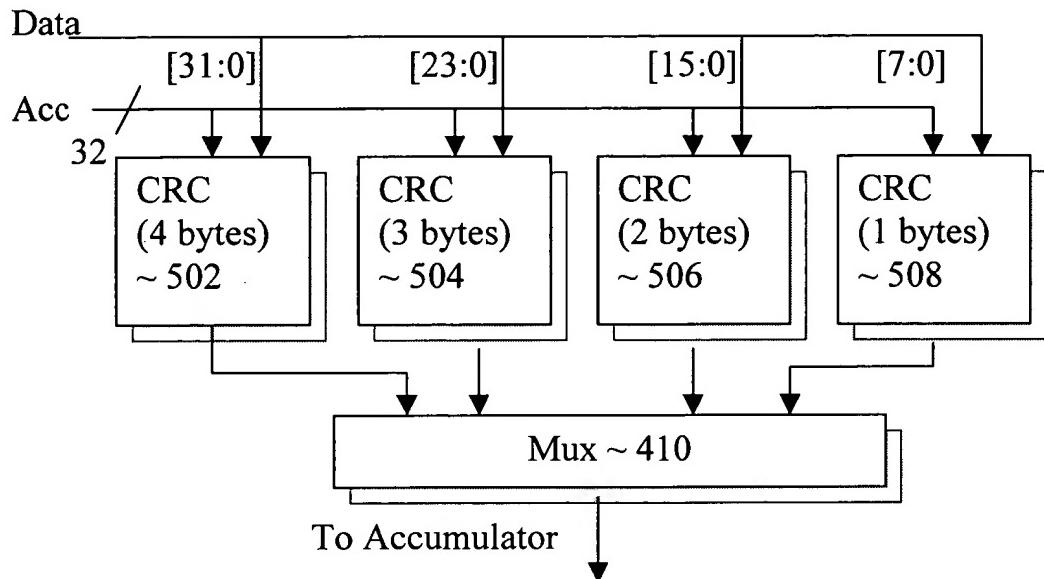


Figure 4b



CRC Calc (4-1 bytes) ~ 306a/306b



**Figure 5a**



CRC Calc (4-1 bytes) ~ 306a/306b

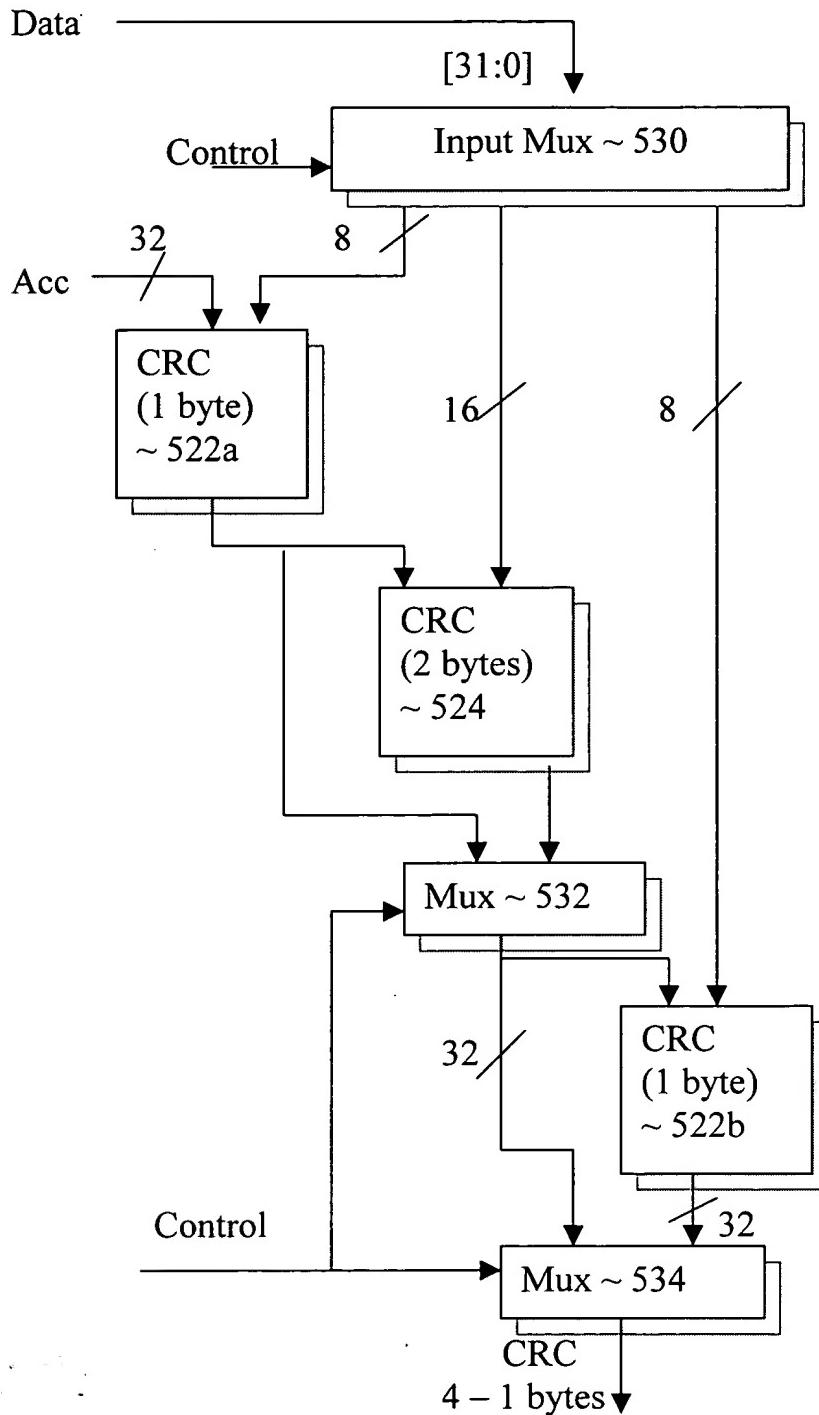


Figure 5b